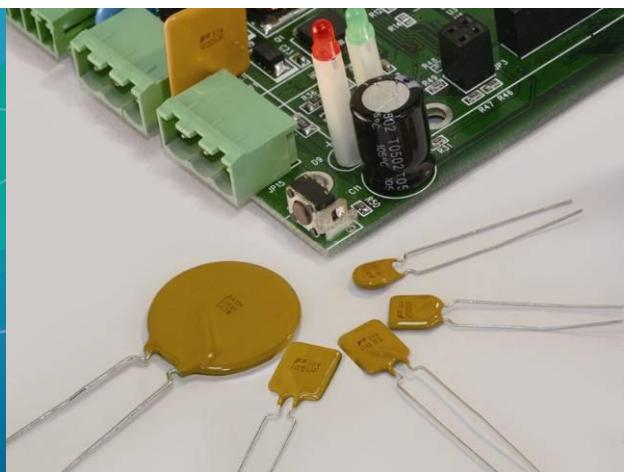




PRODUCT DATASHEET



RLD 72V XMF Series PTC Devices

RLD 72V XMF Series PTC Devices

Description

The RLD72P XMF series provides radial leaded resettable over-current protection with holding current from 1.6A to 5.0A. This series is suitable for wide range of applications in modern electronics and automotive industry.



Features

- RoHS compliant and lead-free
- Halogen-free
- Fast time-to-trip
- 72Vdc operating voltage
- 40A maximum short current
- Compact design saves board space



Applications

- Powered supplies
- Industrial controls
- General electronics
- Security systems
- Automotive applications
- Motor protection

Agency Approval and Environmental Compliance

Agency	File Number	Regulation	Standard
	E201431		2011/65/EU
	R50103284		IEC 61249-2-21:2003

Electrical Characteristics

Part Number	I_{hold} (A)	I_{trip} (A)	V_{max} (Vdc)	I_{max} (A)	$P_{d\ typ}$ (W)	Maximum Time To Trip		Resistance		Agency Approval	
						Current (A)	Time (Sec.)	R_{min} (Ω)	R_{1max} (Ω)		
RLD72P160XMF	1.60	3.2	72	40	1.8	8.00	4.5	0.070	0.300	✓	✓
RLD72P185XMF	1.85	3.7	72	40	2.0	9.25	4.7	0.060	0.250	✓	✓
RLD72P250XMF	2.50	5.0	72	40	2.2	12.50	5.0	0.045	0.200	✓	✓
RLD72P300XMF	3.00	6.0	72	40	2.4	15.00	5.5	0.035	0.170	✓	✓
RLD72P375XMF	3.75	7.5	72	40	2.7	18.75	6.0	0.022	0.130	✓	✓
RLD72P400XMF	4.00	8.0	72	40	3.0	20.00	6.7	0.018	0.100	✓	✓
RLD72P500XMF	5.00	10.0	72	40	3.7	25.00	8.5	0.010	0.060	✓	✓



RLD 72V XMF Series PTC Devices

Note on Electrical Characteristics

■ Vocabulary

- I_{hold} = Hold current: maximum current device will pass without tripping in 23°C still air.
- I_{trip} = Trip current: minimum current at which the device will trip in 23°C still air.
- V_{max} = Maximum voltage device can withstand without damage at rated current (I_{max})
- I_{max} = Maximum fault current device can withstand without damage at rated voltage (V_{max})
- $P_{d\ typ}$ = Typical power dissipated from device when in the tripped state at 23°C still air.
- R_{min} = Minimum resistance of device in initial (un-soldered) state.
- R_{1max} = Maximum resistance of device at 23°C measured one hour after tripping or reflow soldering of 260°C for 20 sec.

■ **Caution: Operation beyond the specified rating may result in damage and possible arcing and flame.**

■ **Specifications are subject to change without notice.**

Polymeric PTC Selecting Guide

■ Determine the following operating parameters for the circuits:

- Normal operating current (I_{hold})
- Maximum interrupt current (I_{max})
- Maximum circuit voltage (V_{max})
- Normal operating temperature surrounding device (min°C/max°C)

■ Select the device form factor and dimension suitable for the application:

- Surface Mount Device (SMD)
- Axial Leaded Device (ALD)
- Radial Leaded Device (RLD)
- Other Customized Form Factors
- DISC Device

■ Compare the maximum rating for V_{max} and I_{max} of the PPTC device with the circuit in application and make sure the circuit's requirement does not exceed the device rating.

■ Check that PPTC device's trip time (time-to-trip) will protect the circuit.

■ Verify that the circuit operating temperature is within the PPTC device's normal operating temperature range.

■ Verify the performance and suitability of the chosen PPTC device in the application.

WARNING

■ Mechanical Stress

- PPTC devices will undergo a thermal expansion during fault condition. If PPTC devices are installed or placed in an application where the space between PPTC devices and the surrounding materials (e.g., covering materials, packaging materials, encapsulate materials and the like) is insufficient, it will cause an inhibiting effect upon the thermal expansion. Pressing, twisting, bending and other kinds of mechanical stress will also adversely affect the performance of the PPTC devices, and shall not be used or applied.

■ Chemical Pollutants

- Silicone-based oils, oils, solvents, gels, electrolytes, fuels, acids, and the like will adversely affect the properties of PPTC devices, and shall not be used or applied.

■ Electronic and Thermal Effect

- PPTC devices are secondary protection devices and are used solely for sporadic, accidental over-current or over-temperature error condition, and shall NOT be used if or when constant or repeated fault conditions (such fault conditions may be caused by, among others, incorrect pin-connection of a connector) or over-extensive trip events may occur.
- PPTC devices are different from fuses and, when a fault condition occurs, will go into high-resistance state and do not open circuit, in which case the voltage at such PPTC devices may reach a hazardous level.
- Operation over the maximum rating or other forms of improper use may cause failure, arcing, flame and/or other damage to the PPTC devices.
- Conductive material contamination, such as metal particle, may induce shortage, flame or arcing.
- Due to the inductance, the operation circuits may generate a circuit voltage (Ldi/dt) above the rated voltage of PPTC devices, which shall not be used under such circumstances.

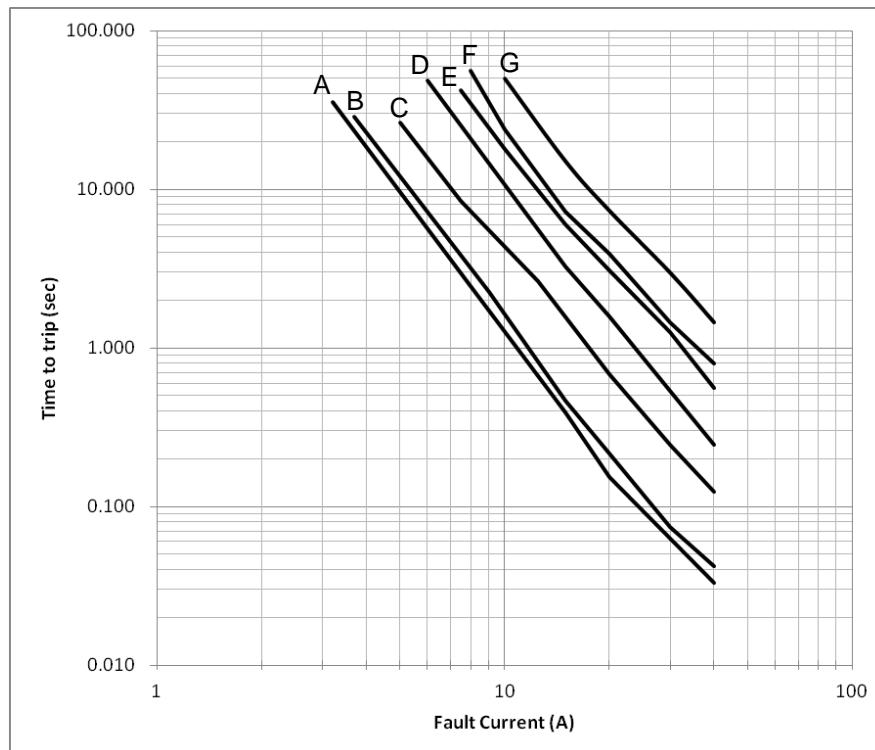
■ General

- Customers shall evaluate and test the properties of PPTC devices independently to verify and ensure that their individual applications will be met.
- The performance of PPTC devices will be adversely affected if they are improperly used under electronic, thermal and/or mechanical procedures and/or conditions non-conformant to those recommended by manufacturer.
- Customers shall be responsible for determining whether it is necessary to have back-up, failsafe and/or fool-proof protection to avoid or minimize damage that may result from extra-ordinary, irregular function or failure of PPTC devices.
- Any and all responsibilities and liabilities are disclaimed if any item under this notice of warning is not complied with.



RLD 72V XMF Series PTC Devices

Average Time-to-Trip Curves

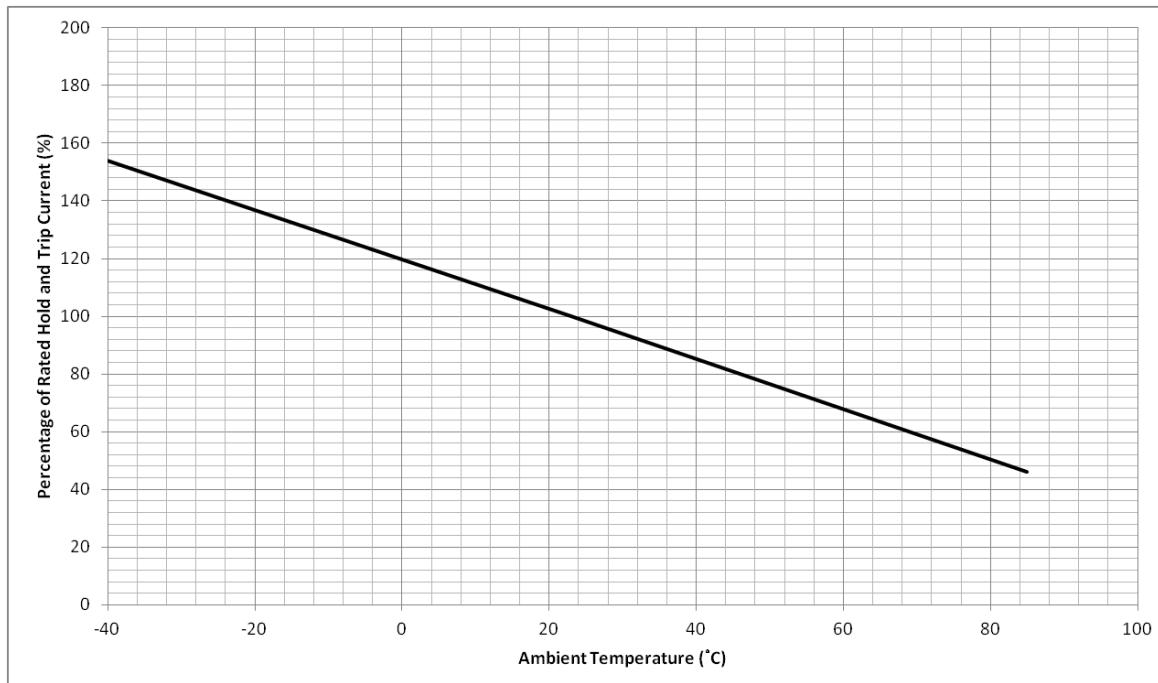


- A = RLD72P160XMF
- B = RLD72P185XMF
- C = RLD72P250XMF
- D = RLD72P300XMF
- E = RLD72P375XMF
- F = RLD72P400XMF
- G = RLD72P500XMF



RLD 72V XMF Series PTC Devices

Thermal Derating Curve



Thermal Derating Table

Recommended Hold Current (A) vs. Ambient Temperature (°C)

Part Number	Ambient Operation Temperature								
	-40 °C	-20 °C	0 °C	23 °C	40 °C	50 °C	60 °C	70 °C	85 °C
RLD72P160XMF	2.46	2.19	1.92	1.60	1.36	1.22	1.08	0.95	0.74
RLD72P185XMF	2.85	2.53	2.21	1.85	1.58	1.41	1.25	1.09	0.85
RLD72P250XMF	3.85	3.42	2.99	2.50	2.13	1.91	1.69	1.48	1.15
RLD72P300XMF	4.62	4.11	3.59	3.00	2.56	2.29	2.03	1.77	1.38
RLD72P375XMF	5.78	5.13	4.49	3.75	3.19	2.87	2.54	2.21	1.73
RLD72P400XMF	6.16	5.47	4.79	4.00	3.41	3.06	2.71	2.36	1.84
RLD72P500XMF	7.70	6.84	5.99	5.00	4.26	3.82	3.39	2.95	2.30



RLD 72V XMF Series PTC Devices

Physical Dimensions (mm.)

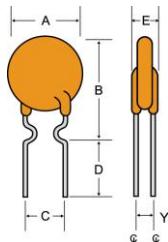


Fig. 1

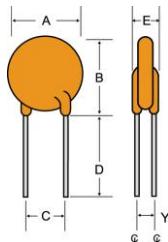


Fig. 2

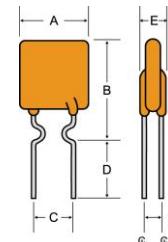


Fig. 3

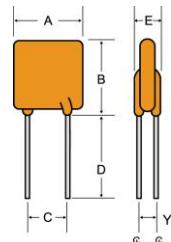


Fig. 4

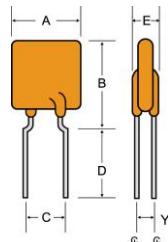


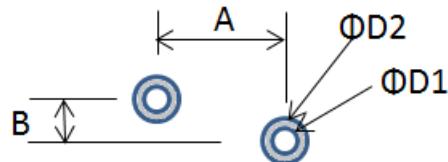
Fig. 5

Part Number	A Max.	B Max.	C Typ.	D Min.	E Max.	Y Typ.	Fig.	Lead Dia.
RLD72P160XMF	10.2	15.7	5.1±0.7	7.6	3.8	1.4	3	0.81
RLD72P160XMF-S	10.2	13.8	5.1±0.7	7.6	3.8	1.4	4	0.81
RLD72P185XMF	11.2	16.5	5.1±0.7	7.6	3.8	1.4	3	0.81
RLD72P185XMF-S	11.2	14.4	5.1±0.7	7.6	3.8	1.4	4	0.81
RLD72P250XMF	12.5	19.1	5.1±0.7	7.6	3.8	1.4	3	0.81
RLD72P250XMF-S	12.5	16.5	5.1±0.7	7.6	3.8	1.4	4	0.81
RLD72P300XMF	14.0	22.0	5.1±0.7	7.6	3.8	1.4	3	0.81
RLD72P300XMF-S	14.0	18.3	5.1±0.7	7.6	3.8	1.4	4	0.81
RLD72P375XMF	14.0	25.6	5.1±0.7	7.6	3.8	1.4	3	0.81
RLD72P375XMF-S	14.0	23.0	5.1±0.7	7.6	3.8	1.4	4	0.81
RLD72P400XMF	15.1	26.0	10.2±1.0	7.6	3.8	1.4	3	0.81
RLD72P400XMF-S	15.1	23.4	10.2±1.0	7.6	3.8	1.4	4	0.81
RLD72P500XMF	19.1	28.6	10.2±1.0	7.6	3.8	1.4	3	0.81
RLD72P500XMF-S	19.1	25.0	10.2±1.0	7.6	3.8	1.4	4	0.81



RLD 72V XMF Series PTC Devices

Recommend Pad Layout (mm)

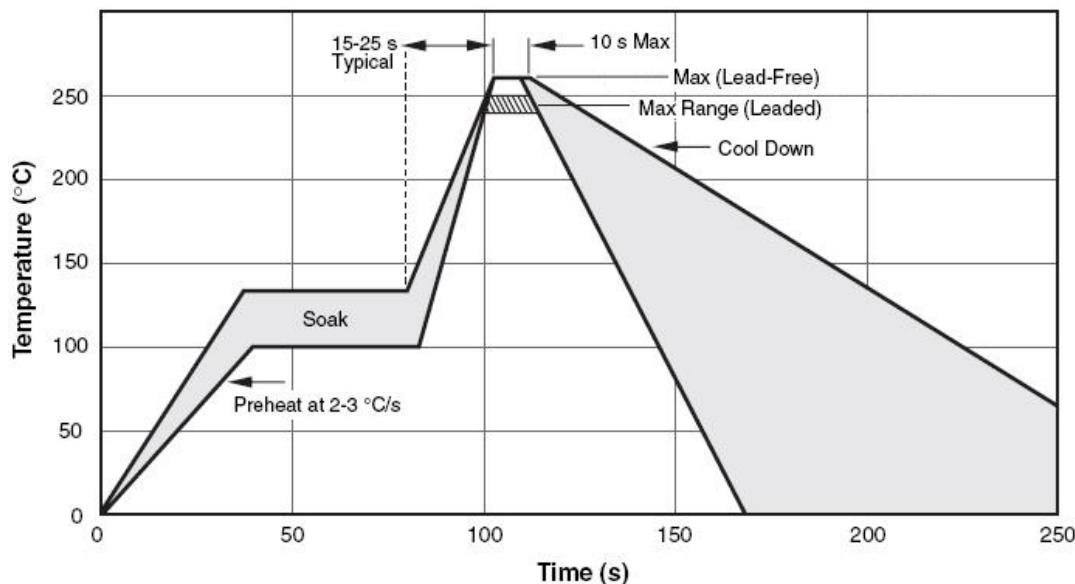


Part Number	A (Typ.)	B (Typ.)	D1 (Typ.)	D2 (Typ.)
RLD72P160XMF	5.1	1.4	1.5	3.5
RLD72P185XMF	5.1	1.4	1.5	3.5
RLD72P250XMF	5.1	1.4	1.5	3.5
RLD72P300XMF	5.1	1.4	1.5	3.5
RLD72P375XMF	5.1	1.4	1.5	3.5
RLD72P400XMF	10.2	1.4	1.5	3.5
RLD72P500XMF	10.2	1.4	1.5	3.5



RLD 72V XMF Series PTC Devices

Wave Soldering Parameters



Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate ($T_{S\max}$ to T_P)	4°C/second max.
Preheat	
-Temperature Min ($T_{S\min}$)	100°C
-Temperature Max ($T_{S\max}$)	125°C
-Time ($T_{S\min}$ to $T_{S\max}$)	60-180 seconds
Peak Temperature (T_P)	265°C
Max Time at Peak Temperature (t_P)	5 seconds
Ramp-Down Rate	6°C /second max.
Time 25°C to Peak Temperature	5 minutes max.
Storage Condition	0°C ~35°C, ≤80%RH

Note: If the wave soldering temperatures exceed the recommended profile, devices may not meet the performance requirements.



RLD 72V XMF Series PTC Devices

Environmental Specifications

Operating Temperature	-40°C to +85 °C
Maximum Device Surface Temperature in Tripped State	125°C
Passive Aging	+85°C , 1000 hours ±5% typical resistance change
Humidity Aging	+85°C , 85%R.H. 1000 hours ±5% typical resistance change
Thermal Shock	MIL-STD-202 Method 107G +85°C /-40°C 10 times -30% typical resistance change
Solvent Resistance	MIL-STD-202, Method 215 No change
Vibration	MIL-STD-883C, Method 2007.1, Condition A No change
Moisture Sensitivity Level	Level 1, J-STD-020C

Physical Specifications

Lead Material	P160XMF-P500XMF: Tin-plated copper
Soldering Characteristics	Solderability per MIL-STD-202, Method 208E
Insulating Material	Cured, flame retardant epoxy polymer meets UL94V-0 requirements.



RLD 72V XMF Series PTC Devices

Tape and Reel Specifications: EIA468-B/IEC60286-2

Dimension Description	EIA Mark	IEC Mark	Dimensions	
			Dim.(mm)	Tol.(mm)
Carrier tape width	W	W	18	-0.5/+1.0
Hold down tape width	W ₄	W ₀	11	min.
Top distance between tape edges	W ₆	W ₂	3	max.
Sprocket hole position	W ₅	W ₁	9	-0.5+0.75
Sprocket hole diameter*	D ₀	D ₀	4	-0.32/+0.2
Abscissa to plane(straight lead)	H	H	18.5	+3.0
Abscissa to plane(kinked lead)	H ₀	H ₀	16	+0.5
Abscissa to top P160XMF	H ₁	H ₁	32.2	max.
Abscissa to top P185XMF-P500XMF	H ₁		47.5	max.
Overall width without lead protrusion: P160XMF	C ₁		42.5	max.
Overall width without lead protrusion: P185XMF-P500XMF			57	
Overall width with lead protrusion: P160XMF	C ₂		43.2	max.
Overall width with lead protrusion: P185XMF-P500XMF			58	
Lead protrusion	L ₁	I ₁	1.0	max.
Protrusion of cut out	L	L	11	max.
Protrusion beyond hold-down tape	I ₂	I ₂	Not specified	
Sprocket hole pitch: P160XMF-P250XMF	P ₀	P ₀	12.7	+0.3
Sprocket hole pitch: P300XMF-P500XMF	P ₀	P ₀	25.4	+0.5
Pitch tolerance			20 consecutive.	+1
Device pitch: P160XMF-P250XMF			12.7	
Device pitch: P300XMF-P500XMF			25.4	
Tape thickness	t	t	0.9	max.
Tape thickness with splice	t ₁		2.0	max.
Splice sprocket hole alignment			0	+0.3
Body lateral deviation	Δh	Δh	0	+1.0
Body tape plane deviation	Δp	Δp	0	+1.3
Ordinate to adjacent component lead*: P160XMF-P375XMF	P ₁	P ₁	3.81	+0.7
Ordinate to adjacent component lead*: P400XMF-P500XMF			7.62	+0.7
Lead spacing: P160XMF-P375XMF	F	F	5.08	+0.8
Lead spacing: P400XMF-P500XMF	F	F	10.18	+0.8
Reel width P160XMF-P375XMF	w ₂	w	56	max.
Reel width P400XMF-P500XMF	w ₂	w	63.5	max.
Reel diameter	a	d	370	max.
Space between flanges less device*	w ₁		4.75	-3.25/+9.25
Arbor hole diameter	c	f	26	+12.0
Core diameter*	n	h	91	max.
Box			56/372/372	max.
Consecutive missing places			None	
Empty places per reel			0.1%max.	



RLD 72V XMF Series PTC Devices

Tape and Reel Specifications: EIA468-B/IEC60286-2

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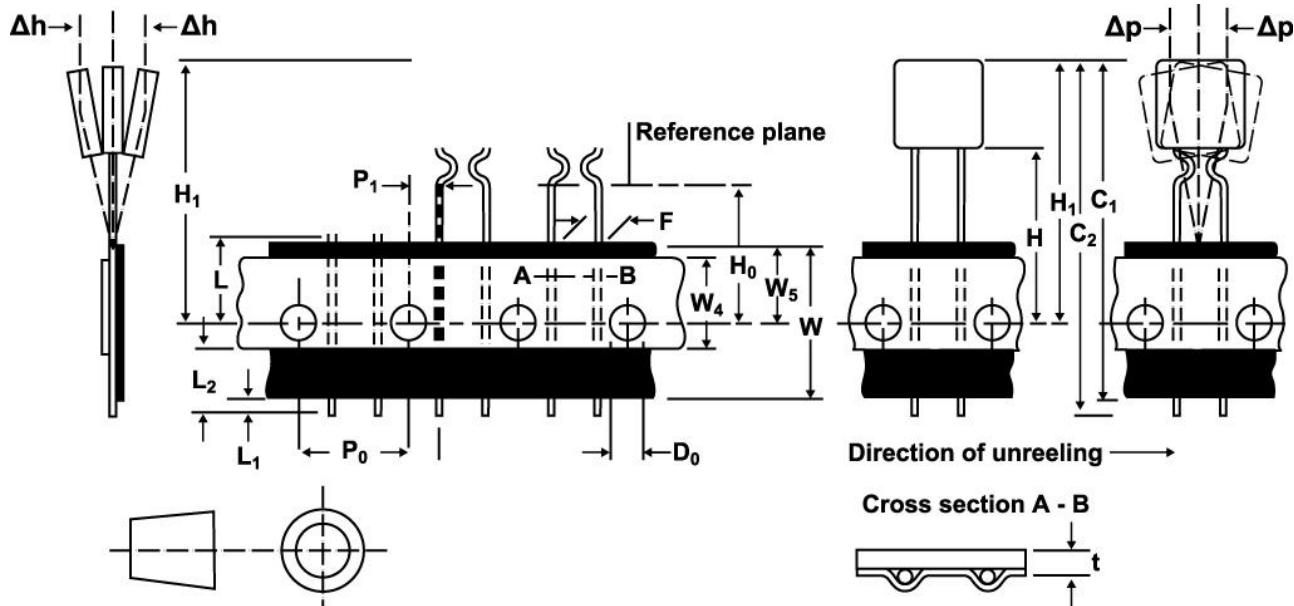


Fig. 1

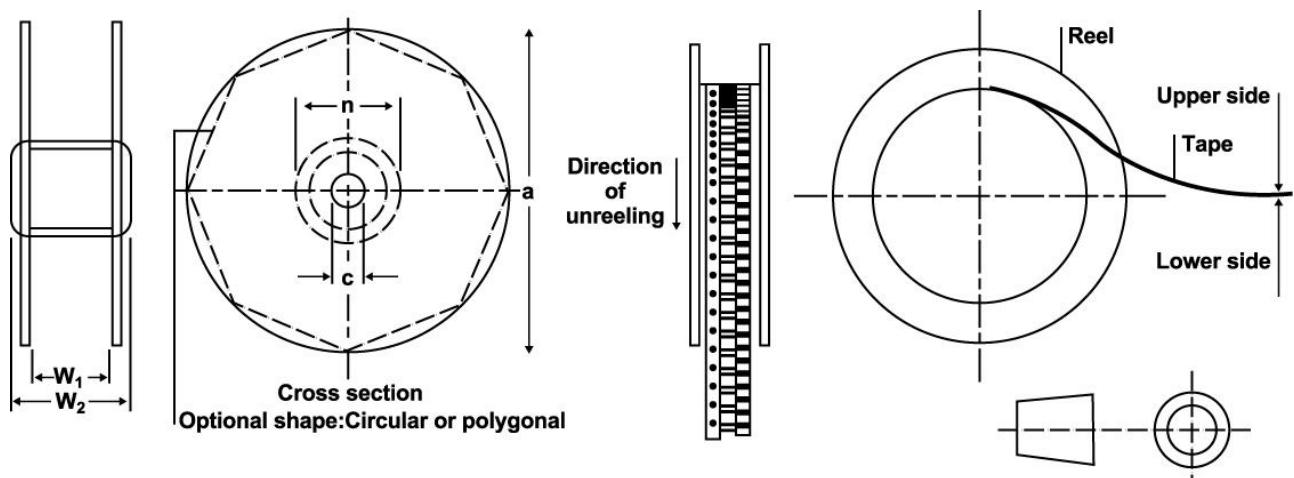
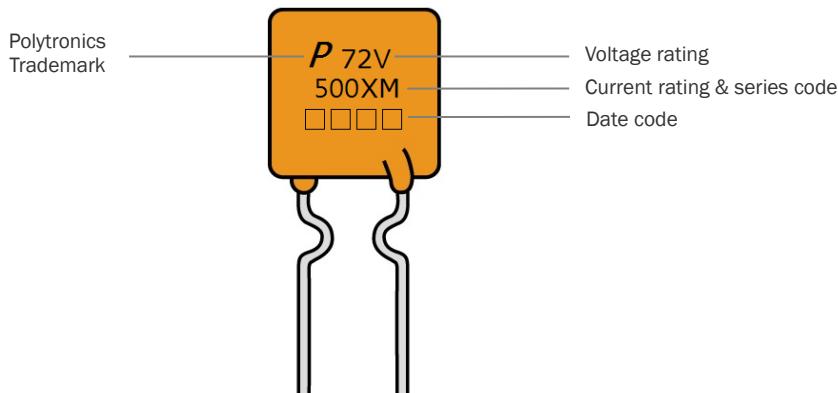


Fig. 2



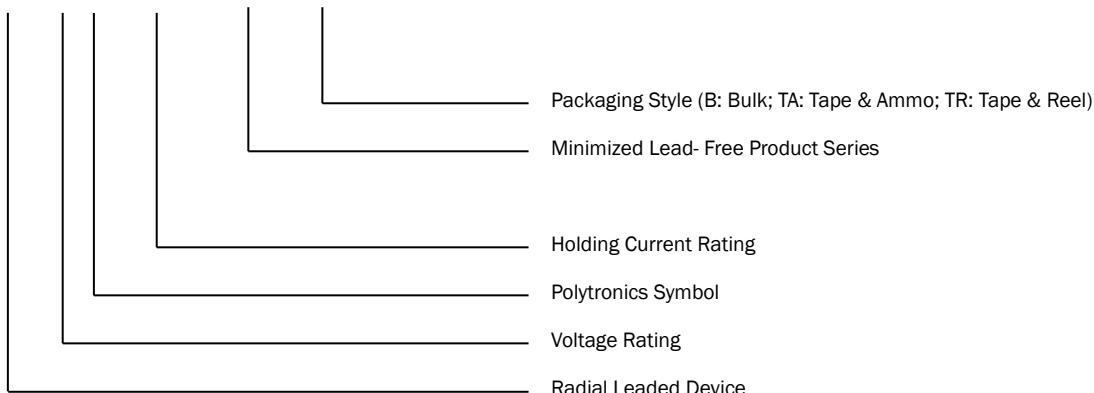
RLD 72V XMF Series PTC Devices

Marking on Device



Part Ordering Number System

RLD 72 P □□□ XMF - □□



RLD 72V XMF Series PTC Devices

Packaging Quantity

Part Number	Ordering Code	Bag Quantity	Reelpack Quantity	Ammopack Quantity
RLD72P160XMF	RLD72P160XMF-B	500		
	RLD72P160XMF-TR		1500	
	RLD72P160XMF-TA			1500
RLD72P185XMF	RLD72P185XMF-B	500		
	RLD72P185XMF-TR		1500	
	RLD72P185XMF-TA			1500
RLD72P250XMF	RLD72P250XMF-B	500		
	RLD72P250XMF-TR		1500	
	RLD72P250XMF-TA			1500
RLD72P300XMF	RLD72P300XMF-B	200		
	RLD72P300XMF-TR		1000	
	RLD72P300XMF-TA			1000
RLD72P375XMF	RLD72P375XMF-B	200		
	RLD72P375XMF-TR		1000	
	RLD72P375XMF-TA			1000
RLD72P400XMF	RLD72P400XMF-B	200		
	RLD72P400XMF-TR		1000	
	RLD72P400XMF-TA			1000
RLD72P500XMF	RLD72P500XMF-B	200		
	RLD72P500XMF-TR		1000	
	RLD72P500XMF-TA			1000

